

Pin	A Pin	Name	Name	B Pin	Pin
1	+3V3	+3V3	+3V3	2	4 HRTIM_FLT1_I2S1_WSI_SPI1_NSS, LTCD_G3, SA1_SD_B, FMC_NE3, DCMI_D2, LTD_C2, B2
3	PD2	EXT12	EXT10	PG10	5 DEBUG_TRACE01, HRTIM_CHB1, TIM3_CH3, TIMB_CH3, USART8_CK, USART5_RTS, FMC_NCE/FMC_D2, SWPMI1_RX, SDMMC1_D0, DCMI_D2
5	PC9	EXT19	EXT18	PC8	6 DEBUG_TRACE01, HRTIM_CHB1, TIM3_CH3, TIMB_CH3, USART8_CK, USART5_RTS, FMC_NCE/FMC_D2, SWPMI1_RX, SDMMC1_D0, DCMI_D2
7	PC13	EXT13	EXT11	P111	8 LTD_G6, USB_OTG_HS_ULPI_DIR
9	PC14	EXT14	EXT10	PA0	10 TIM2_CH1/TIM2_ETR, TIMS_CH1, TIM8_BKIN, USART2_CTS/USART2_NSS, USART4_TX, SDMMC2_CMD, SA1_SD_B, ETH CRS
11	PH4	EXT14	EXT15	PC15	12
13	PA7	EXT17	EXT13	PA3	14 TIM2_CH4, TIMS_CH4, LPTIM5_OUT, USART2_RX, LTD_C2, USB_OTG_HS_ULPI_D0, ETH_COL, LTD_C2
15	GND	GND	GND	16	18 LPTIM1_IN2, HRTIM_EEV4, I2S1_CKSP1_SCK, SPDIFRX1_IN0, SDMMC2_D2, ETH_TX_EN, DCMI_D3, LTD_C3
17	PG12	LPTIM1CH2	LPTIM1CH2	PG11	20 TIM2_CH2, TIMS_CH2, LPTIM3_OUT, UTM15_HHN, USART2_RX, QUADSP1_BK1_J03, SA12_MCLK_B, ETH_REF_CLK/ETH_RX_CLK, LTD_C2
19	PA15	TM2CH1	TM2CH2	PA1	22 DEBUG_TRG0, HRTIM_CHA2, TIM2_CH2, TIMB_CH2, DFSDM1_DATIN2, I2S3_MCK, USART8_RX, SDMMC1_D1230IR, FMC_NE1, SDMMC2_D7, SWPMI1_RX, SDMMC1_D7, DCMI_D1, LTD_C6
21	PC6	TM3CH1	TM3CH2	PC7	24 LPTIM1_OUT, TIM2_CH2, I2C4_SDA, QUADSP1_BK1_J03, SA12_SCK_A, FMC_A18
23	PD12	TM4CH1	TM4CH2	PD13	26 DEBUG_TRACE00, SA1_SD1, DFSDM1_DATIN1, DFSDM1_CKNA, I2S2_SD0/SPI2_MOSI, SA1_SD_A, SA4_SD_A, SDMMC2_CK, SA4A_D1, ETH_MDC, MDIO_S_MDC
25	PC0	ADC1	ADC5	PC1	28 TIM1_BKIN, TIM3_CH1, TIM8_BKIN, I2S1_SD0/SPI1_MISO, SPI8_MISO, TIM1_BKIN_COMP2/MOSI, TIM1_BKIN_COMP2, DCMI_PIXCLK, LTD_C2
27	PA2	ADC2	ADC8	PA8	30 DFSDM1_CKNA2, I2S1_MCK, SPDIFRX1_IN2, ETH_RXD0, FMC_SDNE0
29	PB1	ADC3	ADC7	PB6	32 TIM1_CH2N, TIM3_CH3, TIM8_CH2N, DFSDM1_CKOUT, USART4_CTS, LTD_C3, USB_OTG_HS_ULPI_D1, ETH_RXD2, LTD_C2
31	PC5	ADC5	ADC9	PC9	34
33	AGND	AGND	ADCREF	ADCREF	36
35	AN1P	AN1P	AN1N	AN1N	38
37	AN2P	AN2P	AN2N	AN2N	40
39	AN3P	AN3P	AN3N	AN3N	42
41	AN4P	AN4P	AN4N	AN4N	44
43	AN5P	AN5P	AN5N	AN5N	46
45	AIN6P	AIN6P	AIN6N	AIN6N	48
47	AGND	AGND	AGND	AGND	50
Pin	C Pin	Name	Name	D Pin	Pin
95	+5V	+5V	+5V	96	94 TIM1_CH1N, LPTIM2_OUT, I2S2_CK/SPI2_SCK, DFSDM1_CKIN1, USART3_CTS/USART3_NSS, FDCAN2_RX, USB_OTG_HS_ULPI_D6, ETH_TXD1, USART5_TX
93	PAB9	USB0_VBUS	USB0_VBUS	PB13	92 TIM1_CH2N, TIM12_CH1, TIM8_CH2N, USART1_TX, I2S2_SD0/SPI2_MISO, DFSDM1_DATIN2, USART3_DE/USART3_RTS, USART4_DE/USART4_RTS, SDMMC2_D0, USB_OTG_HS_DM
91	PA11	USB0_DM	USB0_DM	PB14	90 RTC_REFIN, TIM1_CH3N, TIM12_CH2, TIM8_CH3N, USART1_RX, I2S2_SD0/SPI2_MOSI, DFSDM1_CKIN2, USART4_CTS, SDMMC2_D1, USB_OTG_HS_DM
90	PA12	USB0_DP	USB0_DP	PB15	88 TIM1_BKIN, I2C1_SMB, I2S2_WS/SPI2_NSS, DFSDM1_DATIN1, USART3_CK, FDCAN2_RX, USB_OTG_HS_ULPI_D5, ETH_RXD0, USB_OTG_HS_ID, TIM1_BKIN_COMP1/TIM1_BKIN_COMP2, UART5_RX
87	PA14	USB0_ID	USB0_ID	PB12	86
85	GND	GND	GND	84	84 TIM1_CH1, SPI5_SCK, SA1_MCLK_B, USART7_TX, SA1_MCLK_B, USART7_RX, I2S1_CKSP1_BK1_J03
83	PB6	UART5_RX	UART7_RX	PF7	82 RCC_MCO_1, TIM1_CH1, HRTIM_CHB2, TIM8_BKIN2, I2C1_SDA, USART1_CK, USB_OTG_FS_SOF, USART7_RX, TIM8_BKIN2_COMP1/TIM8_BKIN2_COMP2, LTD_C3, LTD_C2
81	PB5	UART5_RX	UART7_RX	PA8	80 HRTIM_EEV1, DFSDM1_CKIN5, I2S3_CKSP1_SCK, USART3_TX, USART4_RX, QUADSP1_BK1_J01, SDMMC1_D2, DCMI_D8, LTD_C2
79	PG13	SPI6_SCK	SPI6_SCK	PC10	78 HRTIM_FLT2, DFSDM1_DATIN5, I2S3_SD0/SPI3_MISO, USART3_RX, USART4_RX, QUADSP1_BK2_NCS, SDMMC1_D3, DCMI_D4
77	PB4	SPI6_MISO	SPI6_MISO	PC11	76 DEBUG_TRACE03, HRTIM_EEV2, I2S3_SD0/SPI3_MOSI, USART3_CK, USART5_RX, SDMMC1_CK, DCMI_D9
75	PG14	SPI6_MOSI	SPI6_MOSI	PC12	74 TIM1_CH1N, TIM4_CH2, HRTIM_EEV1, I2C1_SDA, I2C4_SDA, USART1_RX, DFSDM1_CKIN5, FMC_NL, DCMI_VSYNC
73	PB6	I2C1_SCL	I2C1_SDA	PC13	72
71	AGND	AGND	AGND	70	70 TIM1_CH1/TIM2_ETR, TIM8_CH1N, I2S1_CKSP1_SCK, SPI6_SCK, USB_OTG_HS_ULPI_CK, LTD_C2
69	PA4	DA1	DA2	P45	68
67	AGND	AGND	ADCREF	ADCREF	66
65	AOUT1P	AOUT1P	AOUT1N	AOUT1N	64
63	AOUT2P	AOUT2P	AOUT2N	AOUT2N	62
61	AOUT3P	AOUT3P	AOUT3N	AOUT3N	60
59	AOUT4P	AOUT4P	AOUT4N	AOUT4N	58
57	AOUT5P	AOUT5P	AOUT5N	AOUT5N	56
55	AOUT6P	AOUT6P	AOUT6N	AOUT6N	54
53	AOUT7P	AOUT7P	AOUT7N	AOUT7N	52
51	AOUT8P	AOUT8P	AOUT8N	AOUT8N	50
49	AGND	AGND	AGND	AGND	48